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STAR CRYOELECTRONICS





LTD-17 PB-8

Series SQUID Array Amplifiers Optimized for MHz Frequency-Domain Multiplexed Detector Readout

S. T. P. Boyd¹, John C. Groh², John A. Hall³, Aritoki Suzuki⁴, and Robin Cantor³

1. Introduction

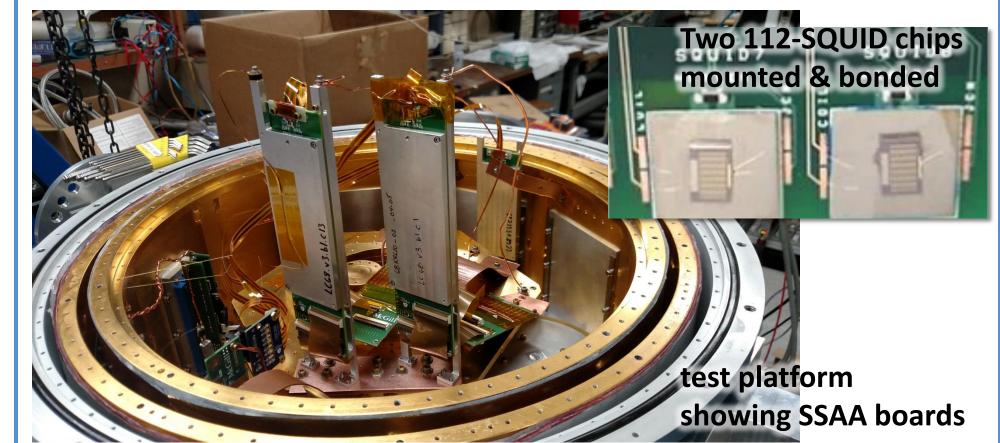
The CMB-S4 next-generation ground-based cosmic microwave background experimental program aims to provide definitive measurements of the early universe using telescopes with \sim 500,000 detectors. In this report, we describe the initial results of a project to develop low-noise Series **S**QUID **A**rray **A**mplifiers (**SSAA**s) that are suitable for Frequency Domain Multiplexed (FDM) readout for large CMB instruments. The new series SQUID array amplifier designs exhibit excellent performance characteristics – smooth output characteristics that simplify tuning and operation, high transimpedance (400 V/A), low current noise referred to the input (5.9 pA/Hz^{1/2}), low input inductance (11 nH), and low power dissipation (18 nW) that for the first time will enable these devices to be co-located on the focal plane of CMB experiments, thereby reducing unwanted parasitics from cabling.

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4. Characteristics at 4K

First generation performance ranking
1. first-order gradiometric SQUIDs with solid (non-

6. 4K Results with FDM Readout



2. Optimization Goals

For the initial part of the project:

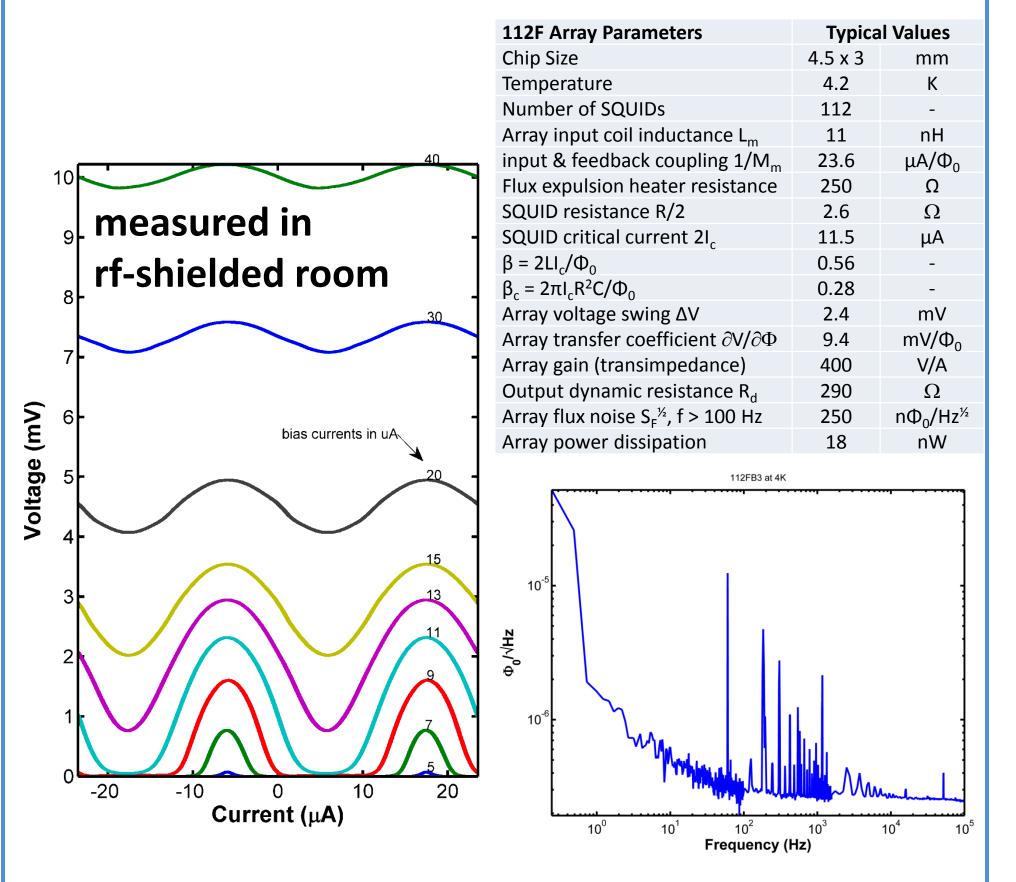
reduced input inductance

• Reduced input inductance allows closer channel packing and stable detector operation in FDM. [1]

smooth V-Φ curves

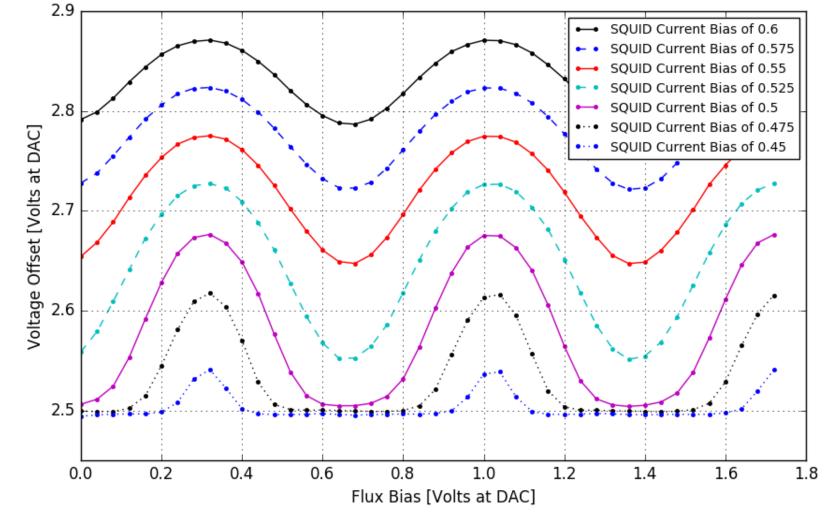
 Huber *et al.* [2] demonstrated a relationship between irregular features in the V-O curve of SSAAs and RF resonances, which in turn can lead to irregular features in the transimpedance at frequencies above a few MHz. Smooth V-O curves thus indicate that SSAA bandwidth can be expanded to support more channels.

- skeletonized) washers
- 2. second-order gradiometric SQUIDs with solid washers
- 3. 1st-order gradiometric SQUIDs with skeletonized washers
- Second generation
 - specialized to first-order gradiometric SQUIDs with solid washers
 - 36 designs varying SQUID damping, details of SQUID wiring, and input coil damping



- PTC cryostat with frequency multiplexing readout electronics used by POLARBEAR-2, SPT-3G, and Simons Array. [4-6]
- Cooldown #1: nothing attached to input coils
- Cooldown #2: connected to mock detectors in full readout chain (nominal telescope configuration)

Tuning and Transimpedance



- Smooth V-phi curves persist in FDM environment
- 300-400 Ohm of usable transimpedance, depending on choice of current bias (nonlinearity and dynamic range concerns)

reduced power dissipation

• For future large-pixel-count CMB experiments, it is desirable to co-locate the SSAAs on the 100 mK stage in close proximity to the pixels and the band pass filters to reduce parasitic impedance from connecting transmission lines.

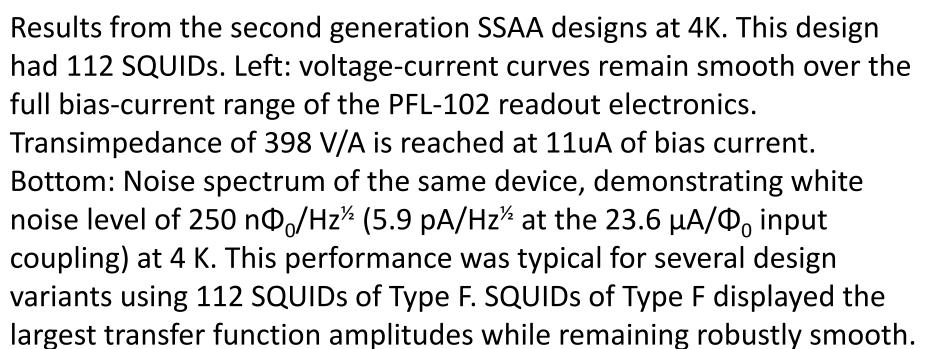
while maintaining the transimpedance (~500 V/A), input coupling (25 μ A/ Φ_0), and current noise (~3.7 pA/Hz^{1/2}) of the benchmark NIST SSAA [2].

3. Technical Approach

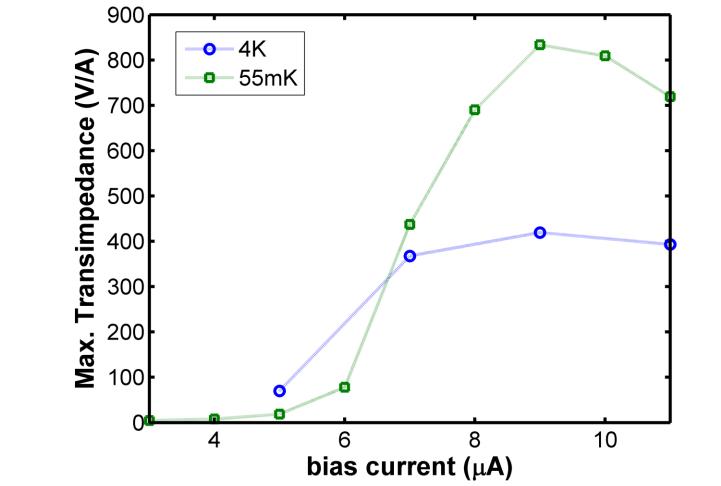
Design Calculations

- Lumped-Element Simulations
- Custom code developed by SB to simulate nonlinear SQUID dynamics, run at Center for Advanced Research Computing at the University of New Mexico [3].
- Harmonic Circuit Analysis
- Commercial software was used to study: 1) SQUID-SQUID broadcast via the input circuit, 2) the impact of transmission-line resonances of the input coils on the function of the input circuit and SQUID-SQUID broadcast, and 3) screening of the SQUID inductance by the resistively-shunted input coils.
- Finite-Element Calculations
- Customized commercial software was used to calculate SQUID and input coil inductances, input coupling, capacitance, and transmission-line parameters.

Design Strategies

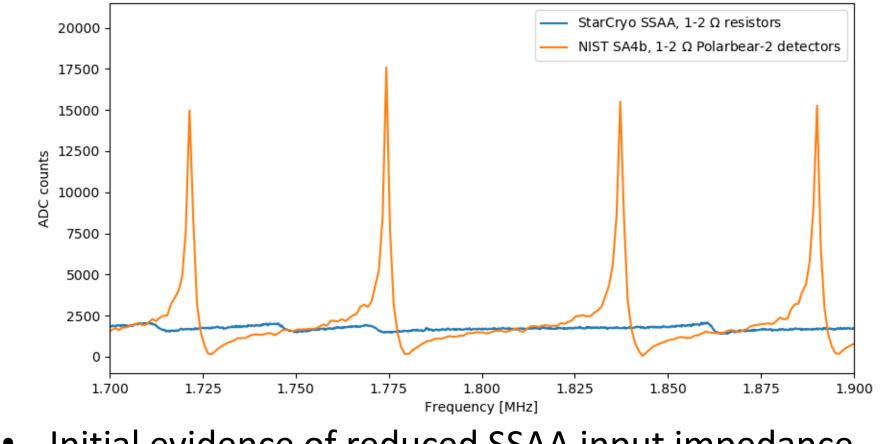


5. Characteristics below 100 mK



Maximum transimpedance of the 112-SQUID SSAA **doubles** when we operate at lower temperature (here 55mK) comparable to future CMB focal plane temperatures.

Nuller Network Analysis



Initial evidence of reduced SSAA input impedance
Z_{SSAA}. In the limit that (Z_{SSAA} + Z_{wiring}) / (Z_{bolometer}) -> 0, this line should be flat.

Acknowledgments

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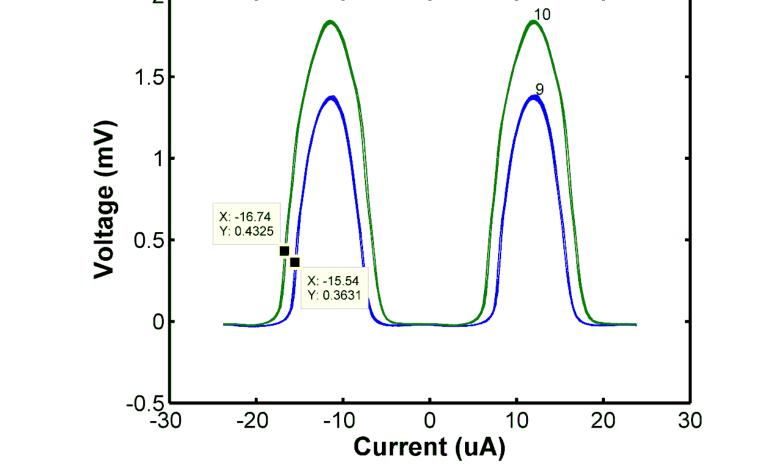
References



- Reduce input inductance, power dissipation, and probability of resonances
- larger-inductance SQUID loop
- lower critical current reduces power dissipation
- low-turn-count input coils reduce input inductance and possible resonance modes
- Wide-ranging designs: 72 different designs across two generations combining
- 1. both second-order and first-order gradiometer SQUID designs;
- 2. damping resistances on the input coils, both extra-coil and intra-coil[2];
- 3. skeletonized SQUID washers to reduce capacitive coupling of the SQUIDs to the input circuit
- "mosaic" arrays, alternating two different SQUID designs with equal input mutuals in a checkerboard pattern, so that nearest neighbors have different Josephson frequencies, but the V-Φ transfer functions add constructively.

Fabrication

- Two generations of SSAAs fabricated using STARCryo commercial SQUID process
- 36 different designs per generation
- Yields on devices tested to date are 100%.



Voltage-current characteristics at 9uA and 10uA bias currents yielding maximum transimpedance at 55mK. Characteristics remain **smooth**. Operating points for transimpedance ~**800 V/A** are indicated. Dissipated power at these bias points is **<5nW**.

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