

8-26-2008

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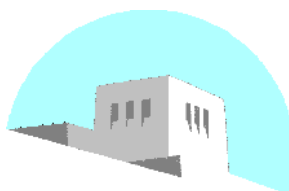
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Crichigno, Jorge; Joud Khoury; Min-Yo Wu; and Wei Shu. "A Dynamic Programming Approach for Routing in Wireless Mesh Networks." (2008). http://digitalrepository.unm.edu/ece_rpts/29

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DEPARTMENT OF ELECTRICAL AND
COMPUTER ENGINEERING



SCHOOL OF ENGINEERING
UNIVERSITY OF NEW MEXICO

**A Dynamic Programming Approach for
Routing in Wireless Mesh Networks**

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UNM Technical Report: EECE-TR-08-08

Report Date: August, 2008

Abstract

The routing problem in Wireless Mesh Networks (WMNs) is concerned with finding “good” source-destination paths. It generally faces multiple objectives to be optimized, such as i) path capacity, which accounts for the bits per second (bps) that can be sent along the path connecting the source to the destination node, and ii) end-to-end delay. This paper presents the Mesh Routing Algorithm (MRA), a dynamic programming approach for computing high-capacity paths while simultaneously bounding the end-to-end delay. The proposed algorithm also provides the option for locating multiple link-disjoint paths, such that the amount of traffic through each path is proportional to its capacity. Simulation results show that MRA outperforms other common techniques in terms of computing high capacity paths, while at the same time bounding the end-to-end delay to a desired value.

1 Introduction

IEEE 802.11-based wireless mesh networks (WMNs) consist of mesh routers and mesh clients equipped with one or more IEEE 802.11 radios, where mesh routers are mainly stationary and provide wireless access to clients. Additionally, mesh routers serve as relay nodes, forwarding packets from other nodes and forming a multi-hop environment [1].

Routing in WMNs is concerned with finding “good” source-destination paths and has been an active research area for several years. Much of the work to solve the routing problem in WMNs is focused on the improvement of global resources [2, 3], or on the performance of individual transfers [4, 5, 6]. Either case, the routing problem can be casted as a multi-objective optimization with objectives such as load-balancing, end-to-end delay, and path capacity. The latter refers to the bits per second (bps) that can be sent along the path connecting the source node to the destination node. Load-balancing schemes mitigate hot spots and improve the reliability by routing through multiple paths, while the end-to-end delay and the path capacity are typical Quality of Service (QoS) requirements that need to be optimized or bounded. In general, they can conflict with each other, and meaningful trade-offs have to be made.

To the best of our knowledge, the only technique proposed so far for simultaneously minimizing the end-to-end delay and maximizing the path capacity is the weighted sum, whereby an objective function $f = \beta_1 \cdot (\text{end-to-end delay}) + \beta_2 \cdot (\text{path capacity})$ is optimized [4, 5, 6, 7]. Although the idea is simple, the process of appropriately setting β_1 and β_2 can be complex, since i) both objectives take different orders of magnitude; ii) the weights depend on the importance of the relative objective to the problem at hand. For example, different source-destination pairs requiring delay-bounded paths may impose different delay bounds. Thus, the precise value of the weights capturing QoS requirements can only be performed on a case-by-case basis. Unfortunately, we are not aware of any systematic procedure that can determine β_1 and β_2 ; and iii) the weighted sum technique does not guarantee that any optimal trade-off solution can be obtained [8]. Hence, with the weighted sum technique, solutions that best satisfy QoS requirements are not guaranteed.

To solve this problem, we propose a routing algorithm denominated the Mesh Routing Algorithm (MRA). MRA is a dynamic programming approach [9] for computing high-capacity, end-to-end delay-bounded paths. Additionally, MRA provides the option for finding multiple link-disjoint paths, such that the amount of traffic through each path is proportional to its capacity (load-balancing multi-path option). The proposed algorithm may be useful not only for WMNs, but also for hybrid networks composed of Vehicular Ad hoc Networks (VANETs) and WMNs. Recent work has demonstrated that the performance of VANETs can be improved if packets are routed through roadside infrastructure networks [10]. MRA is suitable for such networks, where delay-bounded paths are required to deliver safety messages [11].

The remainder of this paper is organized as follows. Section 2 discusses related work. Section 3 lists our assumptions, and presents the metrics for the end-to-end delay and the path capacity. Section 4 describes the proposed routing algorithm. Section 5 summarizes the simulation results, and Section 6 concludes and discusses our future work.

2 Related Work

In [12], So et al. proposed a load-balancing scheme for single-radio networks. The scheme requires consecutive nodes on a path to dynamically and synchronously switch their interface. However, when applied to multi-radio networks, the scheme does not exploit the fact that nodes can send and receive simultaneously [13].

De Couto et al. [14] implemented a routing protocol that incorporates a metric called *expected transmission count* (*etx*), which measures the expected number of transmissions to successfully send a packet across a link. The metric reflects the effects of link loss ratios. However, it does not necessarily select good paths when the nominal rate of links can vary¹.

To optimize the end-to-end delay and the path capacity, Draves et al. [4] defined a two-term weighted sum called *weighted cumulative expected transmission time* (*wcett*). The first term accounts for the end-to-end delay, while the second term indirectly accounts for the *intraflow*² interference. By minimizing the intraflow interference, the path capacity is increased. Similar weighted sum metrics are proposed in [5, 6]. In [5], the proposed metric attempts to capture *interflow* interference, while in [6] the metric quantifies intraflow interference. The *wcett* metric is also used in [7], where the authors proposed a traffic splitting algorithm to balance the load in tree-structured mesh networks.

Iannone et al. [15] defined the cost of a link as the inverse of its nominal rate, and showed that by finding low-cost paths, the throughput is improved with respect to the hop metric.

Kyasanur et al. [16] developed a link layer protocol for channel assignment, and a routing metric that extends *wcett* for cases where the switching of interfaces are necessary. The metric is intended to capture the delay incurred in: i) sending a packet; and ii) switching an interface from one channel to another.

3 Metrics for Delay and Path Capacity

3.1 Assumptions

Our work assumes that: i) all nodes are stationary; ii) nodes are equipped with one or more 802.11 radios; iii) if a node has multiple radios, they are tuned to different, non-overlapping channels. The channel assignment is predetermined by an external party; iv) the transmission power is fixed; and v) the RTS/CTS mechanism [17] is disabled, since it does not improve performance [18]. Although our proposed scheme can operate in single-radio networks, we implemented the algorithm and associated protocol to exploit multi-radio capability. In multi-radio networks, the mitigation of intraflow and interflow are of main importance to allow nodes send and receive data simultaneously through different interfaces.

¹The nominal rate of a link refers to the raw data rate the link is operating at. For example, 802.11b links can operate at 11, 5.5, 2 or 1 Mbps.

²Intraflow (interflow) refers to the interference from the contention among nodes routing packets of the same (different) flow (flows).

3.2 Metric for end-to-end delay

Let $G = (V, E)$ be the graph representing the WMN, where V is the set of nodes and E the set of links. Let $C = \{c_1, c_2, \dots, c_k\}$ be the set of k non-overlapping channels, and $l = (u, v, c)$ be a directed link between nodes u and v , tuned to channel c , such that $l \in E$ and $c \in C$. The Distributed Coordination Function (DCF) standard [17] specifies that a unicast transmission through a link l may be retransmitted up to 7 times (including the first transmission). Denote $t_i(l)$ as the delay to send a data frame during the i^{th} retransmission, $t_{DATA}(l)$ as the transmission time of the frame (i.e., the time the sending interface accesses the channel), and $CW_i(l)$ as the time spent in backoff state during the i^{th} retransmission. For a data frame of S bytes and a link operating at a nominal rate of $r(l)$ bps, $t_{DATA}(l) = 8S/r(l)$. Note that $t_{DATA}(l)$ and $CW_i(l)$ are associated with the sending interface (i.e., for $l = (u, v, c)$, $t_{DATA}(l)$ and $CW_i(l)$ may alternatively be expressed as $t_{DATA}(j)$ and $CW_i(j)$, where j denotes the sending interface at node u). By defining $s_k(l)$ as the probability of successfully delivering a frame through link l after k attempts, Draves et al. [4] approximated the expected delay $t(l)$ (including retransmissions) as:

$$\begin{aligned} t(l) &= \sum_{k=1}^7 s_k(l) t_k(l) = \sum_{k=1}^7 s_k(l) \sum_{i=1}^k (t_{DATA}(l) + CW_i(l)) \\ &= ett(l) + ebt(l) \end{aligned} \quad (1)$$

$$\begin{aligned} ett(l) &= t_{DATA}(l) \sum_{k=1}^7 s_k(l) k \approx t_{DATA}(l) \sum_{k=1}^{\infty} s_k(l) k \\ &= t_{DATA}(l) etx(l) \end{aligned} \quad (2)$$

$$ebt(l) = \sum_{k=1}^7 s_k(l) \sum_{i=1}^k CW_i(l) \quad (3)$$

The term $ebt(l)$ stands for *expected backoff time*, and represents the expected time spent in backoff. Similarly, $ett(l)$ stands for *expected transmission time*, and accounts for the expected time for sending a frame without considering the backoff delay. In Equation (2), $ett(l)$ is simplified by assuming that the frame can be retransmitted as many times as needed, which allows to express it in terms of the *expected transmission count (etx)*. For a link l , $etx(l) = \sum_{k=1}^{\infty} s_k(l) k$, and accounts for the expected number of transmissions to successfully send a frame across a link [14]. The ett metric was proposed by Draves et al. [4] to estimate the total delay of a link. However, ett does not account for the backoff delay, which may have a considerable impact. To clarify this fact, decompose $CW_i(l)$ into $CW_i^{frz}(l)$, the time the sending interface freezes its backoff counter because another interface is transmitting in the same channel, and $CW_i^{dec}(l)$, the time the sending interface senses the medium idle and decrements its counter [17]. The contribution of $CW_i^{dec}(l)$ to $ebt(l)$ was proven in [4] to be minimal, and may be ignored. On the other hand, $CW_i^{frz}(l)$ is the term that implicitly accounts for the time other links on the same channel as l access the channel, and could be significant in medium and highly loaded scenarios. For $l = (u, v, c)$, define $\Gamma(l)$ to be the set of active radios in the interference range of u , v or both. Active radios are those interfaces contending for the use of the channel. Some authors simply classify a radio as active if the packets from that radio can be correctly received at the node of interest [23]. This approach, however, may erroneously classify a radio as active if that radio has currently no data to send and is not contending for the channel. To avoid this, we classify a radio as active if it has been sending data at a rate greater than a threshold δ^3 . Assuming that i) $CW_i^{frz}(l)$ is independent of i and denoted as

³We set δ to $0.25r$, where r is the nominal rate the radio is operating at. The idea behind this threshold is to classify as

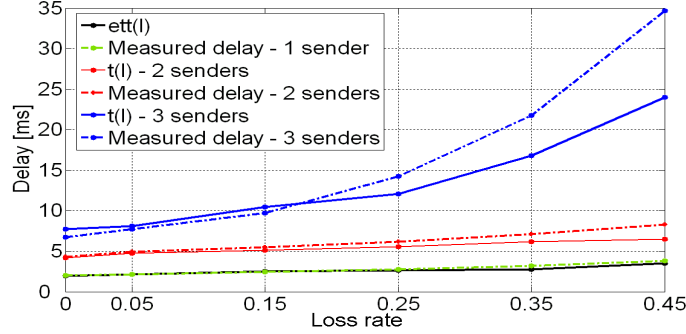


Figure 1: Measured and estimated delays using ett and Equation (5).

$CW^{frz}(l)$, ii) $CW_i^{dec} \approx 0$ [4], and $etx \approx \sum_{k=1}^7 s_k(l)k$, Equation (3) may be rewritten as:

$$\begin{aligned}
 ebt(l) &\approx \sum_{k=1}^7 s_k(l) \sum_{i=1}^k CW^{frz}(l) \approx CW^{frz}(l)etx(l) \\
 &\approx etx(l) \sum_{j \in \Gamma(l)} t_{DATA}(j)
 \end{aligned} \tag{4}$$

In Equation (4), $CW^{frz}(l)$ is approximated as $\sum_{j \in \Gamma(l)} t_{DATA}(j)$, which is based on the fact that the time link l freezes its backoff counter is equivalent to the time the interfering radios are accessing the medium (the DCF [17] requires a sender attempting to send a frame to freeze its backoff counter while other nodes access the channel). Now, substituting Equations (2) and (4) into Equation (1):

$$t(l) = etx(l)(t_{DATA}(l) + \sum_{j \in \Gamma(l)} t_{DATA}(j)) \tag{5}$$

Figure 1 shows, for different link loss rates (i.e., probability that a frame transmission through a link is not successful) and different number of active nodes, i) the estimated delay $t(l)$ given by Equation (5), ii) the actual measured delay according to our simulations, and iii) the $ett(l)$ value (independent of the number of senders), which was proposed in [4] as a delay metric. Our simulations were performed with the CNET simulator [20] and involved simultaneous constant bit rate (CBR) data transfers of 200 packets per second, inducing interflow interference. The packet size was set to 1024 bytes. The simulations lasted 300 seconds, and 802.11a interfaces operating at 6 Mbps were used. The dataset of Figure 1 represents the average delay of the received packets. As expected, $ett(l)$ matched very well the actual delay when there was only one sender, since $ebt(l) \approx 0$ and $t(l) \approx ett(l)$ (see Equation 1). However, as the number of senders is increased, the actual delay increases considerably, and the estimation given by Equation (5) better matches the actual delay. The end-to-end delay of a path P can now be defined as:

$$D(P) = \sum_{l \in P} t(l) \tag{6}$$

active only those radios that are significantly consuming channel resource. We empirically found this value suitable for the simulated scenario. However, δ may depend on the traffic pattern and may need to be set dynamically.



Figure 2: Impact of intraflow. The metric r' in Mbps and the channel are shown over each link. The nominal rate r is assumed to be 2 Mbps for all l .

3.3 Metric for path capacity

Our goal is to find channel-diverse paths, such that the intraflow interference is minimized and the path capacity maximized. Let $N(l)$ be the set of links interfering with link $l \in P$, including l . For a source node sending S -byte frames along P , we define the effective nominal rate of link l under intraflow interference as:

$$r'(l) = \frac{8S}{\sum_{l' \in N(l)} t_{DATA}(l')} = \frac{1}{\sum_{l' \in N(l)} 1/r(l')} \quad (7)$$

To illustrate the use of Equation (7), consider Figure 2, where the channel and the effective nominal rate r' in Mbps are shown over each link, and the nominal rate r is assumed to be 2 Mbps for all l . Hence, the first and third links in path $P1$, which happen to contend for channel c_1 , have an effective nominal rate of 1 Mbps. Similarly, $r'(l) = r(l)$ for all l in path $P2$, since there are no contending links in that path.

Note that the computation of $r'(l)$ involves links in P only, and hence does not consider interflow interference; however, this was implicitly accounted for while computing the delay $t(l)$ of a link l , since Equation (5) considers the interfering set $\Gamma(l)$. In other words, preferring paths with low end-to-end delay leads to the minimization of interflow interference.

Finally, the nominal capacity of a path P , considering intraflow interference, is then defined as:

$$R(P) = \min_{l \in P} \{r'(l)\} \quad (8)$$

$R(P)$ may be viewed as the effective nominal rate of the *bottleneck* link in P and therefore should be maximized. By bottleneck link we refer to the link with minimum capacity considering r' as metric, which does take intraflow interference into account. This is a main difference to the approach where the bottleneck link with respect to the nominal capacity r only is considered (i.e., $R(P) = \min_{l \in P} \{r(l)\}$) and the contention among links in P is neglected (as in [15]). A major contribution of this paper lies in providing an algorithm that finds high-capacity paths with bounded delay $D(P)$.

4 Proposed Algorithm

We start the section by showing how the algorithm operates on the topology shown in Figure 3, where the effective nominal rate in Mbps is shown over each link, and the delay $t(l) = 2$ ms for all l . Suppose first we want to find the path from u to y with highest capacity R and with delay exactly equal to 4 ms. Denote this path as $P_4^*(u, y)$. The way to get to y is through w or x ; so we need to compare these two paths:

$$R(P_4^*(u, y)) = \max \left\{ \begin{array}{l} \min\{R(P_{4-2}^*(u, w)), 6\}, \\ \min\{R(P_{4-2}^*(u, x)), 3\} \end{array} \right\}.$$

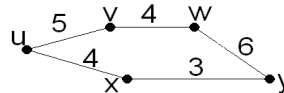


Figure 3: A network with the effective nominal rate r' over each link.

Algorithm 1 MRA

```

1. INPUT:  $G(V, E)$ , source node  $v_s$ , destination node  $v_d$ , delay bound  $\tau$ .
2. OUTPUT: Set  $P_{v_d}^{v_s}$ 
3. /*Initialization */
4. for all pair  $u, w \in V$  and  $d \in Z^+, d < \tau$  do
5.    $P_d^*(u, w) = \text{NIL}$ 
6.   if  $\exists l = (u, w, c) \in E \mid \hat{t}(l) = d$  then
7.      $P_d^*(u, w) = l$  /*break ties by taking the  $l$  with highest  $r(l)$  */
8.   end if
9. end for
10. /*main loop*/
11. for  $d = 1$  to  $\tau - 1$  do
12.   for all pair  $u, w \in V$  do
13.     for all  $l = (v, w, c) \in E \mid d > \hat{t}(l)$  do
14.       if  $P_{d-\hat{t}(l)}^*(u, v) \neq \text{NIL}$  then
15.         if [ $P_d^*(u, w) = \text{NIL}$ ] or [ $R(P_d^*(u, w)) < R(P_{d-\hat{t}(l)}^*(u, v) \oplus l)$ ] then
16.            $P_d^*(u, w) = P_{d-\hat{t}(l)}^*(u, v) \oplus l$ 
17.         end if
18.       end if
19.     end for
20.   end for
21. end for
22. if multi-path option is desired then
23.    $P_{v_d}^{v_s} = \{ \text{Set of best link-disjoint paths in } \{P_d^*(v_s, v_d)\}, d = 1, 2, \dots, \tau - 1 \}$ 
24. else
25.    $P_{v_d}^{v_s} = \{ P_\tau^{**}(v_s, v_d) \}$  /* break ties by taking the path with lowest delay */
26. end if
27. return  $P_{v_d}^{v_s}$ 

```

The nominal capacity of both paths is found by evaluating Equation (8) (min function). However, since there is no path $P_{4-2}^*(u, w)$ (i.e., path from u to w with a delay of 2 ms), then we only consider the path through x , which is the path with maximum capacity (max function). The path $P_{4-2}^*(u, x)$ can be similarly found. In general, finding the highest capacity path whose end-to-end delay is less than a bound τ may be achieved by evaluating $P_d^*(u, v)$ for $d = 1, 2, \dots, \tau - 1$, and then choosing the one with maximum capacity. This idea is generalized as follows. Let $\hat{t}(l)$ be an scaled integer value representing the delay $t(l)$. The procedure to find $\hat{t}(l)$ is summarized at the end of this section. Let $P_d(u, v)$ be a path from u to v with end-to-end delay d , and $P_d^*(u, v)$ the path with maximum capacity among all $P_d(u, v)$. Denote $P_d(u, v) \oplus l$ as the concatenation of $P_d(u, v)$ with link $l = (v, w, c)$ (i.e., $P_{d+\hat{t}(l)}(u, w) = P_d(u, v) \oplus l$), and $P_\tau^{**}(u, w)$ as the maximum capacity path from u to w with a delay bounded by τ . $P_\tau^{**}(u, w)$ is then determined by:

$$R(P_d^*(u, w)) = \max_{l=(v,w,c) \in E} \{R(P_{d-\hat{t}(l)}(u, v) \oplus l)\} \quad (9)$$

$$R(P_\tau^{**}(u, w)) = \max_{d < \tau} \{R(P_d^*(u, w))\} \quad (10)$$

Algorithm 1 returns a path or a set of paths (if multi-path is preferred) such that the end-to-end delay is bounded by τ . The running time of the algorithm is $O(\tau|V|^2)$. Note that the loop at line 13 is executed $O(1)$ times for a typical WMN⁴.

The procedure for scaling and rounding the total delay $t(l)$ of a link l to $\hat{t}(l)$ is explained as follows. From Equation (5), note that $t(l)$ depends on $etx(l)$, $t_{DATA}(l)$ and $t_{DATA}(j)$, $j \in \Gamma(l)$. We normalized the link delay to the minimum delay a link may experience when sending a data frame of size S_{ref} . For

⁴For example, experimental results in [19] concluded that for optimal performance the number of neighbors for a given node should be less than 6.

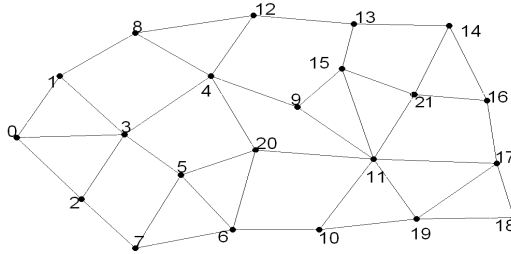


Figure 4: Network topology used for simulation purposes.

example, for 802.11b networks, the minimum delay is obtained when the following conditions hold: i) there is no interference ($\Gamma(l) = \emptyset$); ii) $r(l) = 11$ Mbps (maximum nominal rate); and iii) $etx(l) = 1$. Equation (5) would then result in $t_{min} = S_{ref}8/(11 \text{ Mbps})$. The normalized value would finally be $\hat{t}(l) = \text{round}(t(l)/t_{min})$.

5 Simulation Results

MRA was implemented as a distributed source-routed link-state protocol [4]⁵ in the CNET simulator [20], operating on top of the MAC-802.11 layer. For comparison purposes, we have also implemented the Dijkstra algorithm with delay (Equation (5)), *wcett* [4], *hop count* and *rate cost* [15] metrics. We have chosen the CNET simulator due to its offered simplicity to extend the protocol stack and the wireless module as well as its multi-radio multi-channel support. In addition, its propagation model allows to employ not only the Friis free-space model but also models defined by the user, which permits the use of more realistic scenarios (we have used the two-ray model). Interference is modeled by the protocol model of interference [22]. The simulation data are the result of three simulation scenarios performed on the network topology depicted in Figure 4. The interfaces are assumed to be 802.11a omnidirectional radios operating at fixed nominal rates, which are randomly selected from the standard values⁶. Nodes are equipped with two or three radios tuned to fixed channels; we assume the existence of five channels (c_1 to c_5). The transmission power was set to 30mW. Referring to Figure 4, a link between two nodes implies they have at least one interface tuned to the same channel, and they are within transmission range of each other⁷. The application layer generated CBR flows of 1024-byte packets, and each simulation result was averaged over 10 runs. The evaluation metrics are the *end-to-end delay* and the *packet delivery ratio* (PDR). PDR refers to the ratio of the number of packets received by the destination node to the number of packets generated by the source node, and it reflects the capacity of a path used to route packets. Given that the PDR is proportional to the per-destination throughput (bps at the receiver node [24]), a throughput analysis can be also inferred from a PDR analysis. MRA was tested under three scenarios. The first scenario considers a single flow and is used

⁵In distributed source-routed link-state routing protocols, nodes propagate link information such as delay, capacity, and channel assignment information. Then, based on this topology information, the routing algorithm at each node finds paths for a given destination. For more details about the implementation and overhead of this kind of protocol, please refer to [4].

⁶The standard does not specify how nominal rates should be selected. Although most radios dynamically select the nominal rates, Wu et al. [21] demonstrated that by fixing the rates can be more convenient. Either case, our algorithm is independent of the rate selection mechanism.

⁷For the given configuration and path loss model, the transmission and interference ranges are about 133 and 373 meters respectively.

to better explain our results. The second scenario is intended to test the algorithm’s performance under multiple flows. The third scenario focuses on the algorithm’s performance under high traffic loads. MRA is then compared in the last scenario with approaches that do not guarantee a delay bound but attempt to optimize throughput.

5.1 Scenario I

This first scenario considers a single flow from node 8 to node 11 that lasted 100 seconds. We ran MRA with delay bounds $\tau = 4$ and $\tau = 6$, as well as the shortest path algorithm with delay as its metric and denoted by SP-D. For $\tau = 4$, MRA found only one path, namely the path labeled as *path 1* in Figure 5. On the other hand, the shortest path algorithm found the path labeled as SP-D. Note that the path SP-D might erroneously be considered better than *path 1* if only delay (without taking interference into account) is evaluated. For a delay bound of $\tau = 6$, MRA found two paths, *path 1* and *path 2*. Figures 6(a) and 6(b) show the packet delivery ratio and the min/max end-to-end delay values, respectively. In Figure 6(a), when the packet generation rate (PGR) at the source node is 400 packets/sec, the performance of both algorithms in terms of PDR is optimal. However, as PGR is increased to 500 packets/sec, we notice that SP-D starts dropping packets; its corresponding PDR drops slightly below 1 and gets worse as the rate is further increased. The negative impact of the intraflow interference on SP-D (due to contention between links $(4, 20, c_3)$ and $(20, 11, c_3)$) is manifested by both the decrease in the PDR and the increase in the end-to-end delay; while the minimum and maximum end-to-end delay values are very similar for $\text{PDR} = 400$, the maximum value gets much higher for higher PDR rates. For MRA with $\tau = 4$, the variation of the end-to-end delay (difference between the maximum and the minimum delay values) is minimum, since the links on *path 1* do not interfere with each other (i.e., $W^{frz} \approx 0$). When τ is relaxed to 6, packets experience a delay between 3.5 ms and 4 ms approximately on *path 1*, and between 4.5 ms and 6.4 ms on *path 2*. The reason behind this high variation is the contention between links $(8, 4, c_5)$ (on *path 1*) and $(15, 11, c_5)$ (on *path 2*), which happen to be in the interference range of each other (this increment on the delay can be captured by Equation (6)). Note that MRA exploits the multiple path between the source and the destination to increase the flow capacity, as shown in Figure 6(a) for $\tau = 6$.

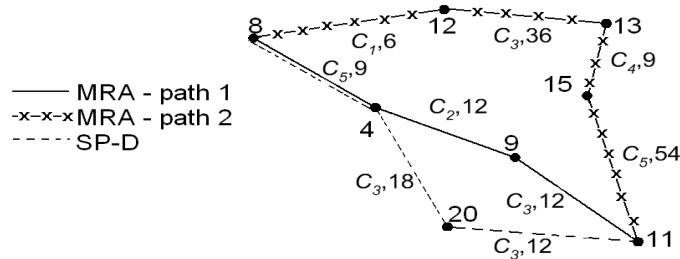


Figure 5: Paths found by MRA and SP-D in Scenario I. The attributes of each link correspond to the channel and the nominal rate (in Mbps), respectively (Figure not drawn to scale).

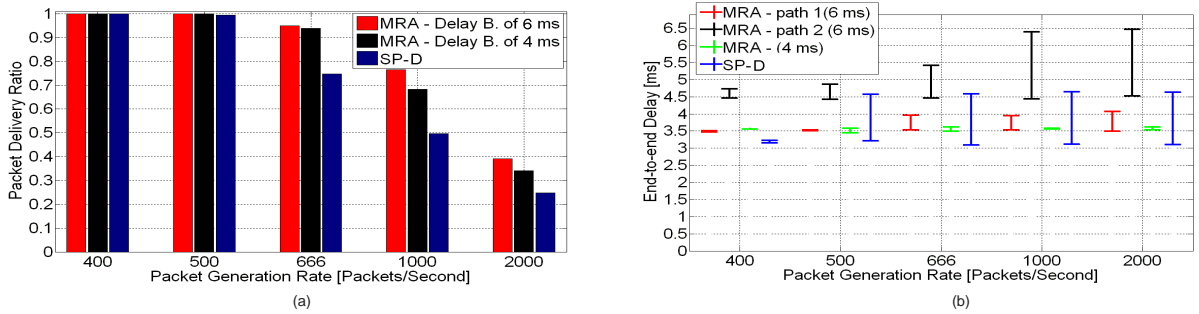


Figure 6: Packet delivery ratio and end-to-end delay for scenario I. For the end-to-end delay, the delay bound is given in parenthesis.

5.2 Scenario II

We simulated 6 concurrent flows with the following source-destination pairs: 8-11, 13-5, 9-10, 6-1, 4-0, 11-16. Each source generated 100 packets per second. Flow 1 started at $t = 1$ minute. The inter-arrival time between flows was set to 1 minute, and the simulation ended at $t = 7$ minutes. Similarly, τ was set to 5 ms. Figures 7(a) and 7(b) show the per-flow results in terms of PDR and end-to-end delay. MRA delivered almost 100 % of the packets of flows 1, 4, 5 and 6; and about 80 % and 93 % of the packets of flows 2 and 3. On the other hand, the PDR for SP-D is below 0.8 for flows 1, 2, 3 and 6. The delay of MRA, though mostly higher than the end-to-end delay of SP-D, was kept below τ , except for some few packets of flows 1 and 2. As in scenario I, the main reason of the higher capacity of paths found by MRA is the diversity of channels used along those paths.

5.3 Scenario III

Again, in this experiment, we simulated the same 6 flows as in scenario II, but in saturated case, where source nodes always have packets to send. Although MRA has been designed to find delay-bounded paths, the idea of this test is to relax the delay bound and compare MRA with SP-wcett [4], SP-cost [15], and SP-hop. SP-wcett minimizes a weighted sum of two terms quantifying end-to-end delay and

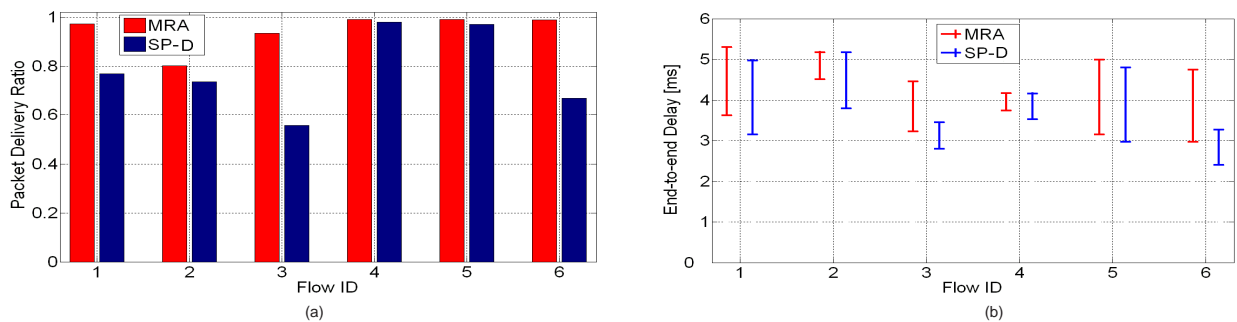


Figure 7: Packet delivery ratio and end-to-end delay for scenario II.

intraflow interference respectively. In SP-cost, a link cost is the inverse of its nominal rate; then, the routing algorithm minimizes the maximum among the cost of the links along the path. Similarly, SP-hop minimizes the number of hops to reach the destination. Since neither of these approaches can find delay-bounded paths, we only compare MRA with them in terms of PDR. We relaxed the value of τ to 3 times the delay of the path with shortest delay (i.e., firstly, the delay of the shortest path is found. Then, τ is defined as 3 times that value). Table I shows the ratio of the PDR values normalized to the PDR obtained by MRA. While SP-wcett delivered about 93% of the packets delivered by MRA, SP-cost and SP-hop delivered 86% and 81%. One reason of the better performance of MRA is that it considers the interference from interfering radios while computing link delays (Equation (5)), which, as discussed before, enforces the minimization of interflow interference. In addition, the load-balancing scheme contributes to the performance of MRA. However, we also note the good performance of SP-wcett; the difference between MRA and SP-wcett relative to SP-cost and SP-hop is that the former approaches consider intraflow interference, which greatly contributes to increasing the path capacity. SP-hop, as expected, had the lowest PDR, because it takes neither interference nor nominal rates into account.

Table I: Normalized PDR - Scenario III.

SP-wcett [4]	SP-cost [15]	SP-hop
0.9345	0.8671	0.8095

6 Conclusion

In this paper, we have presented a routing algorithm for WMNs that computes paths with high capacity while bounding the end-to-end delay to a desired value. Through the dynamic programming model presented in the paper, both the path capacity and the end-to-end delay metrics can be simultaneously considered, providing an alternative approach to the traditional weighted sum technique. Additionally, our proposed algorithm provides the load-balancing through link-disjoint paths option.

To measure link delays, we have extended the procedure proposed in [4] to consider the backoff delay when multiple nodes contend for the channel. We have also defined a metric for path capacity that takes intraflow interference and nominal rate of links into account. Simulation results show that MRA can find delay-bounded paths with higher capacity than other approaches, while splitting the traffic through multiple path.

As a future work, we plan to extensively simulate MRA under different network topologies and to capture the impact that parameters such as data rate and transmission power have over the algorithm. Incorporating power control and scheduling are interesting research directions which we plan to pursue. For link scheduling, a conflict-free MAC protocol to jointly operate with MRA may be needed to improve the coordination among nodes, which is a fundamental cause of unfairness of the DCF [25].

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